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24309 7590 03/02/2005
XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124
04/22/2005 WADDEL3 00000106 240040 09757404
01 FC:1501 1400.00 DA

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Pat Slaback (Depositor's name)
Pat Slaback (Signature)
April 21, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/757,404	01/08/2001	Steven A. Guccione	X-786 US	6382

TITLE OF INVENTION: METHOD AND SYSTEM FOR DEVICE-LEVEL SIMULATION OF A CIRCUIT DESIGN FOR A PROGRAMMABLE LOGIC DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	06/02/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
HOGAN, MARY C	2123	703-014000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list
(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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- 1 LeRoy D. Maunu
2
3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)
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(A) NAME OF ASSIGNEE XILINX, INC.
(B) RESIDENCE: (CITY and STATE OR COUNTRY) San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

- 4a. The following fee(s) are enclosed:
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☐ Advance Order - # of Copies
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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☒ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant, a registered attorney or agent, or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature Date April 21, 2005
Typed or printed name Keith A. Chanroo Registration No. 36,480

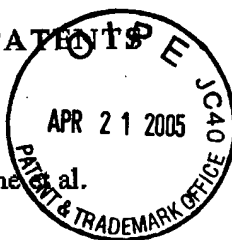
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Inventor(s): Steven A. Guccione et al.

Assignee: XILINX, INC.

Serial No.: 09/757,404

conf.no. 6382

Filed: January 8, 2001

Title: Method and System for Device-Level Simulation of a Circuit Design for a Programmable Logic Device

Docket No.: X-786 US

Enclosed: Return Receipt Postcard
Fee Transmittal Form - Part B

Date: April 21, 2005

VIA FACSIMILE 703-746-4000

Atty/Sec: KAC/pbs

A handwritten signature, possibly "KAC", in dark ink.